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precomputed jump address

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 type requiring a predicted target **address** for the **Precomputed-Branch** architecture is an indirect **jump**, where. the destination may be specified by values ...

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 The programmer replaces the **jump** destination **address** (the trailing 32 bits of 0) at ... By use of constants you can help to compile **precomputed** values. ...

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 JUMPADR is the. symbolic **jump address**. For every component of the path the values of the. input ports are **precomputed**. 5.2 Retargetable Code Generation ...

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Precomputed D-Mapping. Assumes all primitives tessellated to same level ... 4-D **address** register a0 wo 1; 16 vertex inputs v# ro 1; 16 temporary registers ...

www.microsoft.com/mscorp/corpevents/meltdown2001/ppt/DXG9.ppt - [Similar pages](#)

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 Mem will take the data in memory at a **precomputed address** (more on this ... **Jump** - Goto the label specified in the constant column of your microinstruction. ...

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 Control instructions - **jump**, subroutine call, etc. ... |**Jump** to iadr if ZERO flag not set ... as **address** into memory which stores **precomputed** prefixes ...

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 The prediction state comprises target, **address**, and type information as well ... We originally proposed the use of a **Precomputed** Branch architecture in [5]. ...

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 TBD: Figure out where to **jump** to and put **address** in A1 ... we have sped up our code by storing a table of **precomputed** values at compile time rather than ...

cse.stanford.edu/class/cs110/handouts/17SwitchAndSubs.pdf - [Similar pages](#)

James, Doug

Mailing address: Carnegie Mellon University Robotics Institute 5000 Forbes Avenue
Pittsburgh, PA 15213. For more information, see my personal homepage. **Jump ...**
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BTB; Return **Address** Stack; **Precomputed** Branch. Recovery logic ... Exceptions are
usually handled by **jumping** into a service (software) routine. ...

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...

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We designed the entire system, from the processor, to memory and I/O bridge chips, ...

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to reduce I/O wait time. A production ODB run typically. uses hundreds of warehouses

and ... **prediction** experiments. 6.1 **Conditional Branch Prediction** ...

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Branch prediction has been a popular architectural feature used to remedy the negative effects of **conditional** control transfer. We believe that dynamic ...

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Such hardware allows for improvements of **branch prediction** accuracy, but **branch** ...

It also often leads to the elimination of **conditional branch** ...

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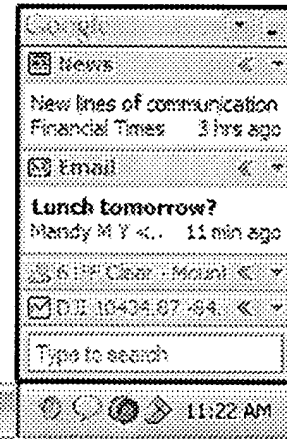
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